



The Computer Engineering Research Center

Mixed-Signal/RF Integrated Circuits Seminar Series

Design Trends in Deep-Submicron CMOS with Example of GPS-Receiver

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Abstract

This talk will highlight the implementation of a fully integrated GPS receiver as an example of system-on-chip. The issues and challenges faced in the design of such SoCs will be highlighted, along with the issues that are exacerbated due to 90-nm lithography. Measurement results and performance of such SoCs (as compared to existing multi-chip solutions) will be discussed. Finally, some open problems that still exist are highlighted.

Biography

Dr. Srinivasan Venkatraman received his Masters and Ph.D degree from Indian Institute of Technology, Chennai in 1991 and 2000.

He has been with Texas Instruments, since July 1991. From 1991 through 1997 he was involved with the design of various mixed signal and audio circuits. Since 1997, he is involved in the design of RF transceivers. He has been awarded 8 patents.

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