

BRIAN LEE

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- Objective** To obtain a summer internship in electrical engineering with an emphasis on Analog / RF IC design
- Education** **Master of Science in Electrical Engineering, Spring 2008**
The University of Texas at Austin GPA: 3.93/4.0
- Bachelor of Computer Engineering, Fall 2005**
University of Minnesota at Twin Cities GPA: 3.91/4.0
- Related Course Work**
RF IC Design, Radio Frequency Circuit Design, Analog IC Design, Mixed Signal System Design, Electromagnetic Field Theory, DSP, Integrated Sensors, VLSI I, A/D and D/A Converters for Mixed Signal Integrated Systems, Analog & Digital Electronics, Microprocessor & Microcontroller System Design, Computer Architecture
- Experience** **Design Internship, Silicon Laboratories** May 2007 – present
Building a Programmable EM Field Scanner for spur management
Evaluated various antenna topologies and performances
Modified front-end matching network for FM receivers
- Research Participant, UT** March 2007 – May 2007
Involved in a 26 GHz UMC 0.13 μ m RF front-end project
Designed microstrip IC transmission lines and PCB interface using ADS & Momentum
- Research Staff, POSTECH** March 2006 – June 2006
Converted X-windows UNIX based Optimization and Extraction program for SPICE model parameters of Integrated Circuit (PHOEMICS) to Windows
Designed XGraph tool for Windows Environment using VC++
Modified DC Operating Point Computation Conversion Algorithms of internal SPICE
- Undergraduate Research Assistant, UofM** March 2005 – December 2005
Designed an Automatic Image on VLSI Chip program for Cadence Virtuoso using SKILL coding – <http://www.circuitbooster.com>
Researched NBTI (Negative Bias Temperature Instability) effects
Built IC chip testing & evaluation environment using LABView with GPIB interfacing
- Undergraduate Research Assistant, UofM** February 2004 – June 2004
Managed FEI gun and debugged relevant software
- President, International Yonsei Community (IYC)** September 2001 – August 2002
Planned and facilitated activities with over 150 international students
Organized 1st Annual IYC Workshop, 6th International Cooking Festival
Developed leadership, teamwork, and communication skills

Relevant Course Projects LNA & Mixer Design (RFIC Design)
Front End Matching Network with Matlab optimization (RF Circuit Design)
3rd Order Sigma-Delta Modulator with latched comparator (Data Converters)
Folded Cascode Op – Amp (Analog IC Design)
Digital PLL (VLSI I)

Skills Cadence, ADS & Momentum, HFSS, NEC, SKILL & OCEAN Script, TCL, LABView,
Matlab, Verilog-XL, spice, MAGIC, Java/C/C++/VC++ programming, Microsoft Office
Fluent in English and Korean

Honors & Scholarships Frank Louk Scholarship for year 2005-2006
Oscar A Schott Scholarship for years 2004-2006
The National Dean’s List for year 2003-2004
IT Dean’s List for 2003, 2004 Fall, 2005 Spring at U of M
Honors student for 1999, 2003 Spring at Yonsei University

Members Member, Institute of Electrical and Electronics Engineers (IEEE), 2005 – present
Member, Golden Key International Honor Society, 2004 – present
Personal Relations, Korean International Student Organization, 2003 – present
Member, International Yonsei Community (IYC), 1999 – present

Employability Status: **US Citizen**