

Interests and Expertise

- CAD for Analog & Mixed-Signal and Digital Circuits: Modeling, Synthesis & Optimization
- Design and CAD for Low Power, Approximate Digital Arithmetic Circuits
- Numerical Analysis, Mathematical Optimization, Machine Learning

Education

- **The University of Texas at Austin** Austin, TX
MS & PhD, Electrical and Computer Engineering *Aug. 2012 – May. 2016*
 - Advisors: Prof. Michael Orshansky, Prof. Constantine Caramanis
 - GPA: 3.93/4.0
 - Related Courses: High Speed Arithmetic, Numerical Analysis: Linear Algebra, Theory of Probability I, VLSI II, Nanometer Scale IC Design, Large-Scale Optimization, Mixed-Signal System Design & Modeling, Data Converters, Analog IC Design, Probability & Stochastic Process I, Combinatorics & Graph Theory, Advanced Algorithm
- **Zhejiang University** Hangzhou, China
B.Eng, Electronic and Information Engineering *Sept. 2008 – Jun. 2012*
 - Major: Electronic & Information Engineering
 - Advisors: Prof. Yun Pan, Prof. Xiaolang Yan
 - Overall GPA: 3.91/4.0 Major GPA: 3.99/4.0 Rank: 1 (among 112)

Publications

- **Ye Wang**, Constantine Caramanis, and Michael Orshansky. “*Enabling Efficient Analog Synthesis by Coupling Sparse Regression and Polynomial Optimization*”. *Design Automation Conference (DAC) 2014*
- **Ye Wang**, Meng, Li, Xinyang Yi, Zhao Song, Michael Orshansky, Constantine Caramanis. “*A Novel Power Grid Reduction Method Based on L1 Regularization*”. *Design Automation Conference (DAC) 2015*

Academic Projects

- Approximate Digital Circuit Synthesis from Algorithmic Level, Spring 2014 – present
- Data-Driven Paradigm for Optimization in Analog Synthesis, Fall 2012 – present

Work Experience

- Summer Intern at Samsung Austin R&D Center, Austin, TX, May. 2014 – Aug. 2014
- Summer Intern in Broadcom at Santa Clara, CA, May. 2013 – Aug. 2013

Skills

- **Languages:** C/C++, Verilog, Python, Tcl, SPICE, Assembly, L^AT_EX
- **Applications:** Cadence tools (Virtuoso, Spectre, Encounter, DRC/LVS, etc.), GNU tools (gcc/g++, gdb, gprof, etc.), Modelsim, HSPICE, Xilinx ISE, MATLAB, Vim
- **Operating Systems:** Linux/Unix, Windows
- **Soft Skills:** strong motivation and ability to acquire new knowledge, strong troubleshooting and analysis skills, strong teamwork skills, good communication and writing skills

Awards

- **TI Outstanding Student Designer Award: First Place** 2013
University of Texas at Austin

Employability Status: F-1 Student Visa